

NG/ND-Frames

320-1600A, 240-690V

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Digitrip OPTIM 550 & 1050 Electronic Trip unit types ND, HND, NDC

Long Delay I ² T, Short Delay I ² T	SC-6331-96 18
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Note:

The following curves meet the requirements of UL, CSA, IEC, CCC and CE.

The following circuit breakers are derived from Eaton, Westinghouse, or Cutler-Hammer history.

Time Current Curves are engineering reference documents for application and coordination purposes only.



Powering Business Worldwide

Note: Unless noted below, all curves remain unchanged from their prior revision.

[illegible]

Catalog Number Selection

This information is presented only as an aid to understanding catalog numbers. It is not to be used to build catalog numbers for circuit breakers or trip units.

Table 1. Series G N-Frame (320-1600A)

Frame					NG H 3 080 39 ZG E C				
NG									
Performance					Amperes				
	600	480	415	240	080 = 800 120 = 120 125 = 1250 ④ 160 = 1600 ④				
S	25	50	50	85	Trip Unit 33 = 310+ Electronic LS 32 = 310+ Electronic LSI 35 = 310+ Electronic LSG 35B22 = 310+ Electronic LS(A), GFA, no trip 36 = 310+ Electronic LSI G 36B22 = 310+ Electronic LSI(A), GFA, no trip 38 = 310+ Electronic ALSI with Maintenance Mode 39 = 310+ Electronic ALSIG with Maintenance Mode 39B22 = 310+ Electronic ALSI(A) with Maintenance Mode and GFA, no trip				
H	35	65	70	100					
C	65	100	100	200					
U①	65	150	150	200					
Notes					Poles				
① 800A only.					3 = Three				
② Neutral inn left pole on GN; right pole on NG.					4 = Four; neutral ② 0% protected				
③ Breakers do not ship with lugs.					7 = Four; neutral ② 100% protected				
④ IEC Only					9 = Four; neutral ② 0/60/100% adjustable protection				
Trip units are factory installable only.									
Rating					Terminations ③				
Blank = 80% rated C = 100% rated					M = Metric tapped line/load conductors E = Imperial tapped line/load conductors				
Feature									
Blank = No feature B20 = High load alarm B21 = Ground fault ZG = Zone selective interlocking									

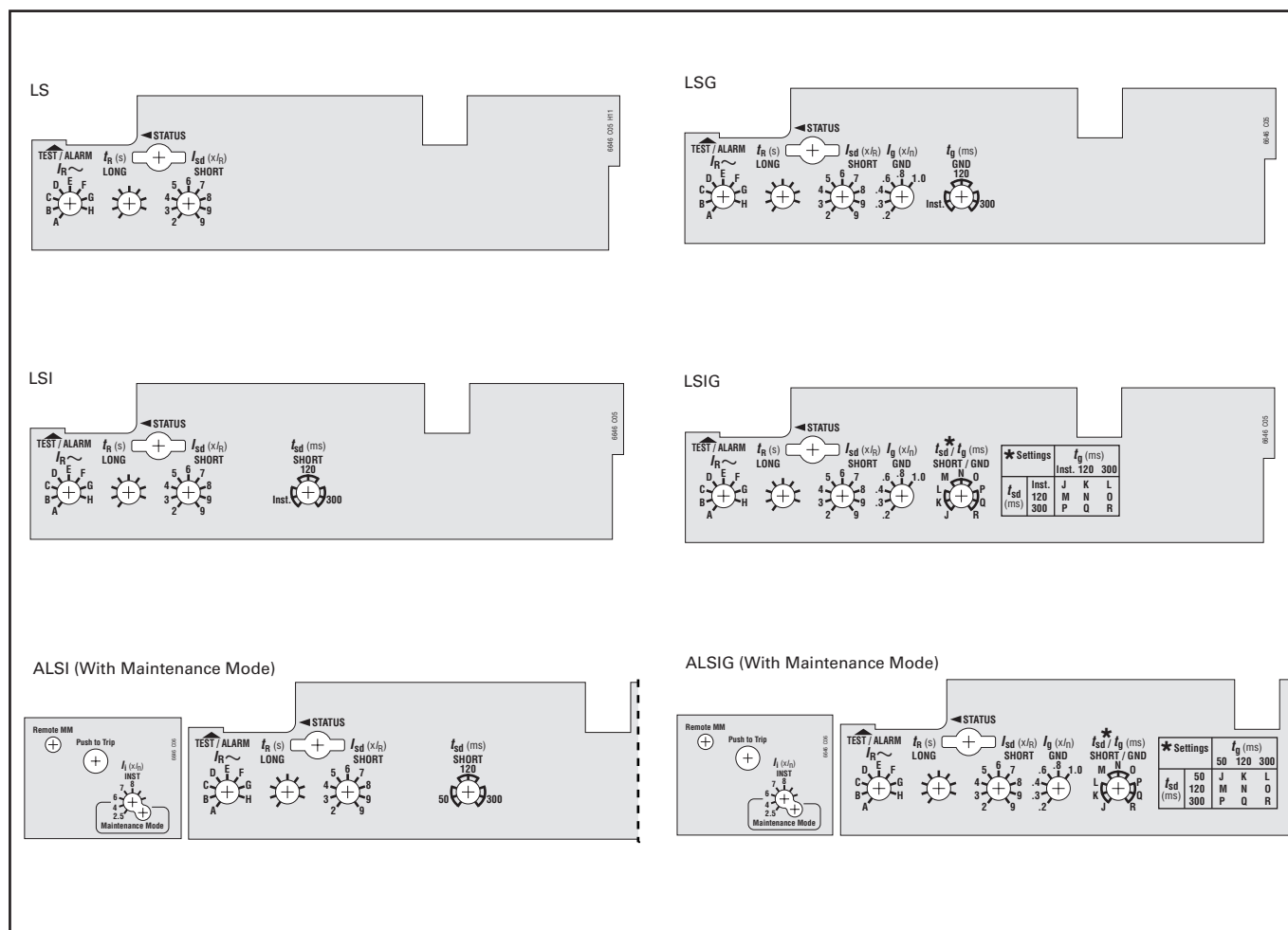


Figure 1. Digitrip 310+ Faceplates



Digitrip 310+ Circuit Breaker Time/Current Curves (Phase Current)

Series G N-Frame Circuit Breakers

Long Delay Response and Short Delay with Flat Response and Override

Catalog Types: NGS, NGH, NGC, NGU, GNS, GNH, GNC, and GNU circuit breakers, three- and four-pole

Trip Unit Type: 32 (LSI), 36 (LSIG), 38 (ALSI), 39 (ALSIG)

**Available Sensors
(I_f / I_n)**

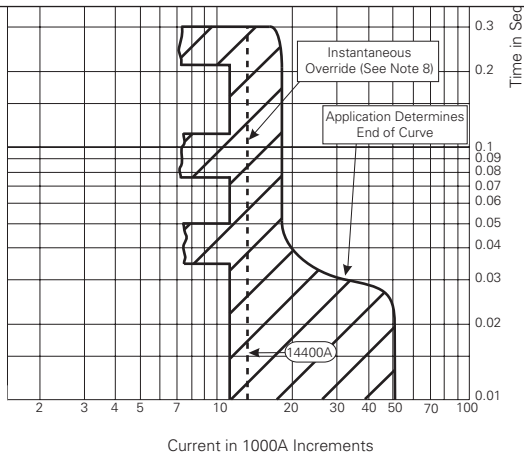
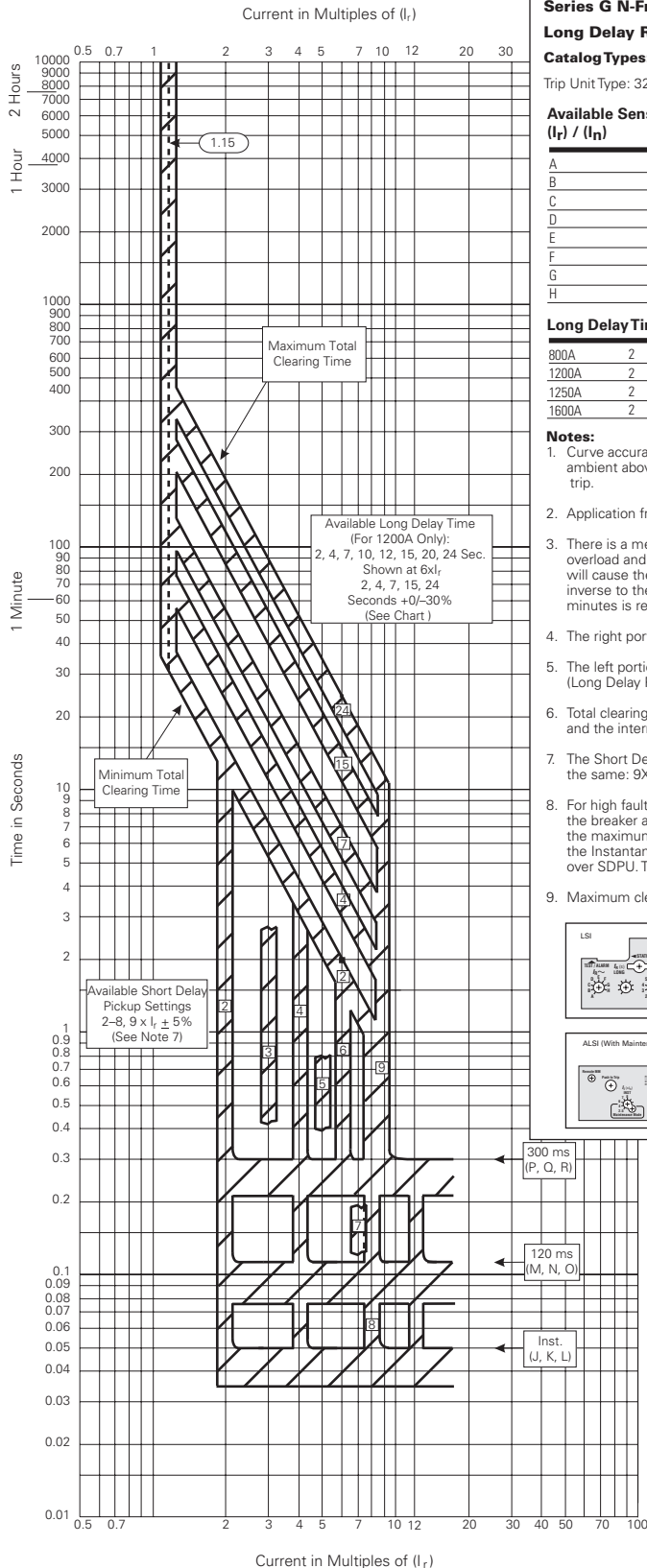
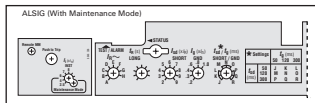
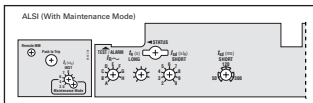
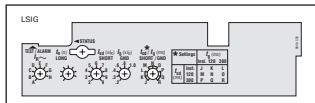
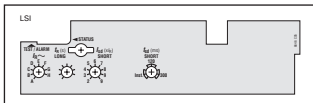
	Rated Amperes			
	800A	1200A	1250A	1600A
A	320A	500A	500A	630A
B	400A	600A	630A	630A
C	450A	630A	700A	700A
D	500A	700A	800A	800A
E	600A	800A	900A	900A
F	630A	900A	1000A	1000A
G	700A	1000A	1200A	1250A
H	800A	1200A	1250A	1600A

Long Delay Time Settings +0%/-30% (seconds)

800A	2	4	6	8	10	12	14	14
1200A	2	4	7	10	12	15	20	24
1250A	2	4	6	8	10	12	14	14
1600A	2	4	7	10	12	15	20	20

Notes:

1. Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton. Temperatures above +85°C cause an over-temperature protection trip.
2. Application frequency is 50/60 Hz.
3. There is a memory effect that can act to shorten the Long Delay. If the breaker trips on a Long Delay overload and is quickly reset, the memory capacitor will still have charge and a subsequent overload will cause the breaker to trip in a shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset memory.
4. The right portion of the curve is determined by the interrupting rating of the circuit breaker.
5. The left portion of the curve is shown as a multiple of the Long Delay Setting. (Long Delay Pickup = 115% of I_f). Range is 110-120%.
6. Total clearing times shown include the response times of the trip unit, the breaker opening, and the interruption of the current.
7. The Short Delay Pickup has nine settings/positions, 2-8; the last two switch positions are the same: 9X.
8. For high fault current levels, an additional fixed instantaneous hardware override is provided to trip the breaker at 14400A. Instantaneous tolerance is +/- 20%. For the 1600A frame only, if I_f is set to the maximum (position H) and SDPU is set to the maximum (position 9), then the SDPU setting and the Instantaneous Override are set to the same value. The Instantaneous Override has precedence over SDPU. Therefore, the breaker will trip on Instantaneous Override.
9. Maximum clearing time when using zone selective interlocking is 62ms.



Adjustable Flat Trip Style (LSI, LSIG, ALSI, ALSIG)

Figure 2. Digitrip 310+ Long Delay Response and Short Delay with Flat Response and Override Curve (LSI, LSIG, ALSI, ALSIG) - Curve Number TC01210010E, March 2012

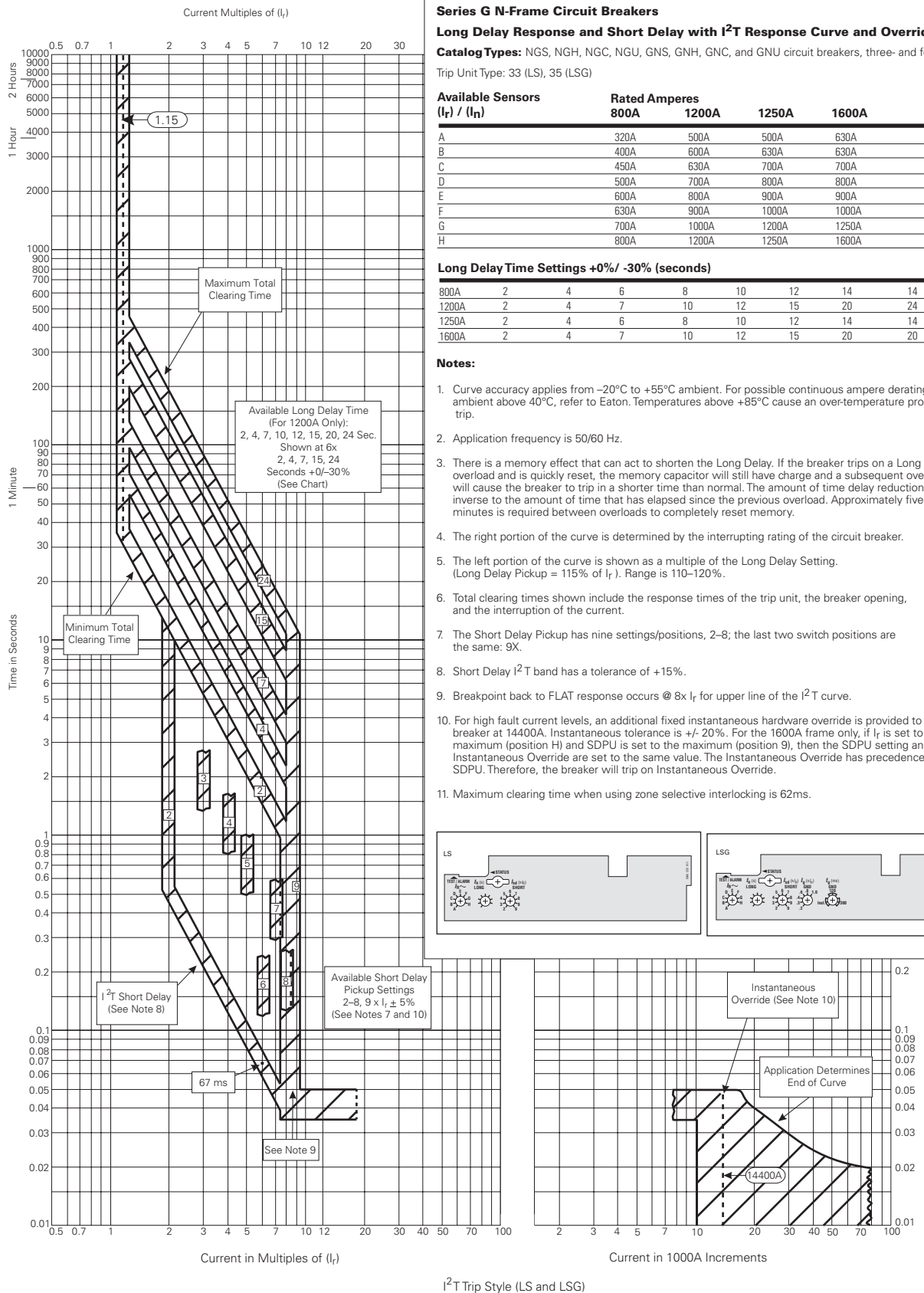
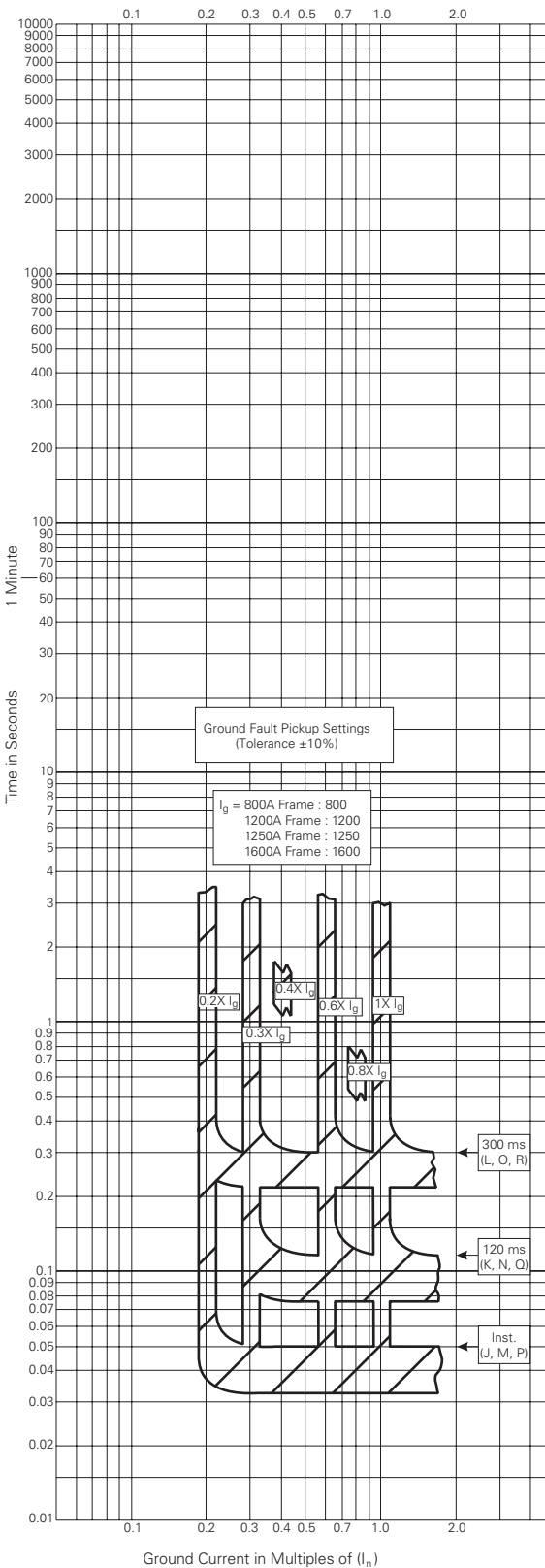


Figure 3. Digitrip 310+ Long Delay Response and Short delay with I²T Response Curve (LS, LSG) - Curve Number TC01210011E, March 2012



EATON
Digitrip 310+ Circuit Breaker Time/Current Curves (Ground Current)
Series G N-Frame Circuit Breakers
Ground Fault Delay Response Curve
Catalog Types: NGS, NGH, NGC, NGU, GNS, GNH, GNC, and GNU circuit breakers, three- and four-pole
Trip Unit Type: 35 (LSG), 36 (LSIG), 39 (ALSIG)

LSG

Diagram of the LSG trip unit showing controls for TEST/ALARM, I_R (s), I_{sd} (xI_g), I_g (xI_n), and I_g (ms). It includes a STATUS indicator and a 300ms delay setting.

LSIG

Diagram of the LSIG trip unit showing controls for TEST/ALARM, I_R (s), I_{sd} (xI_g), I_g (xI_n), and I_{sd}/I_g (ms). It includes a STATUS indicator and a settings table for I_{sd} and I_g.

Settings		I _g (ms)		
		Inst.	120	300
I _{sd} (ms)	Inst.	120	J	K
	300	M	N	L
I _{sd} (ms)	Inst.	120	M	N
	300	P	Q	R

ALSIG (With Maintenance Mode)

Diagram of the ALSIG trip unit showing controls for Remote MM, Push to Trip, I_R (s), I_{sd} (xI_g), I_g (xI_n), and I_{sd}/I_g (ms). It includes a STATUS indicator and a settings table for I_{sd} and I_g.

Settings		I _g (ms)		
		Inst.	120	300
I _{sd} (ms)	Inst.	120	J	K
	300	M	N	L
I _{sd} (ms)	Inst.	120	M	N
	300	P	Q	R

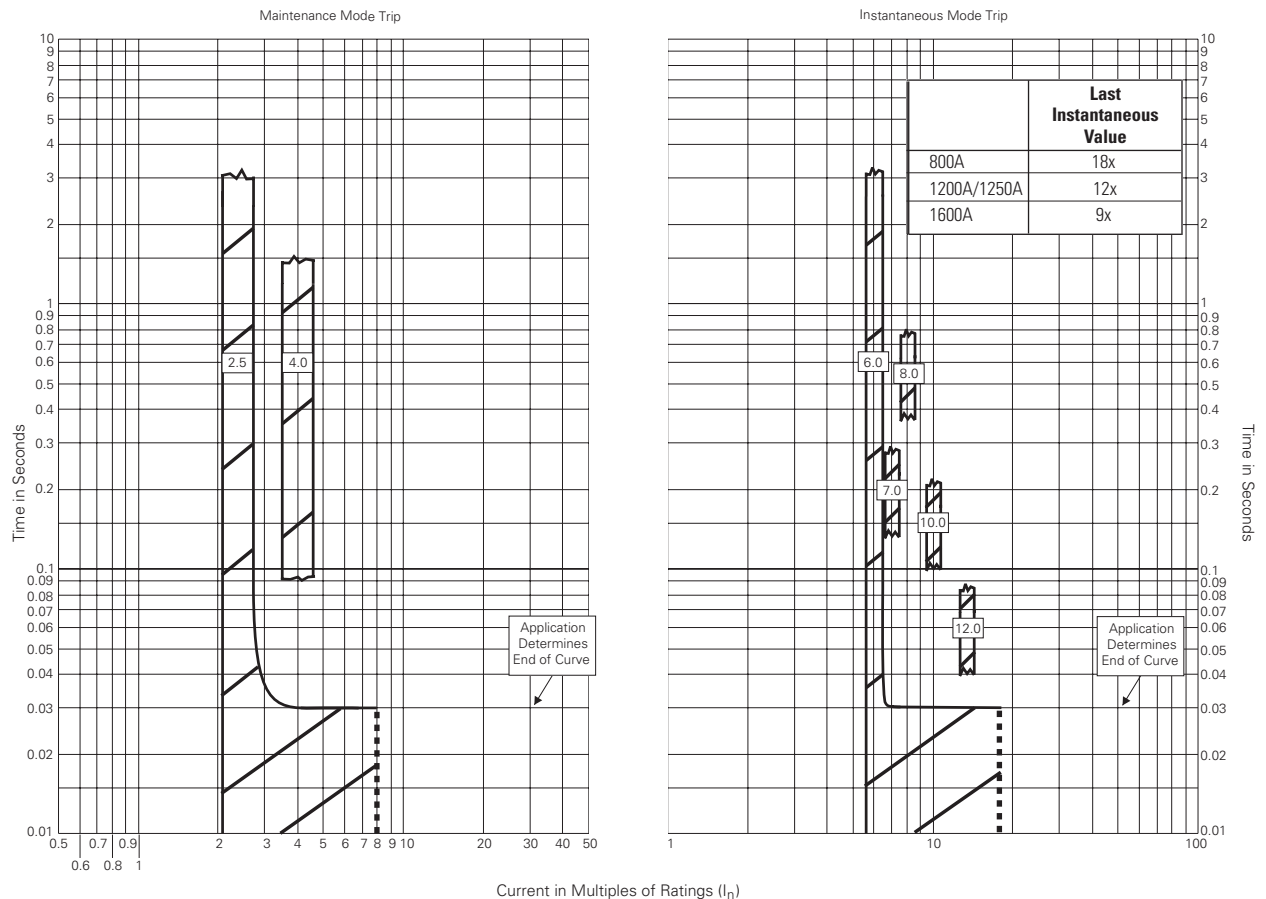
Note: Refer to table below for variations.

Settings		t _g (ms)		
		Inst.	120	300
t _{sd} (ms)	Inst.	J	K	L
	120	M	N	O
	300	P	Q	R

Ground Fault Delay Response Notes:

- Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton. Temperatures about +85°C cause an overtemperature protection trip.
- Application frequency is 50/60 Hz.
- Trip units are suitable for functional field testing with test kit style # 70C1056G52.
- For LD response and SD with flat responses curve, see TC01210010E.
- For LD responses and SD with I²T response curve, see TC01210011E.
- For testing information, please contact Eaton.

Figure 4. Ground Fault Delay Reponse Curve (LSG, LSIG, ALSIG) Curve Number TC01210012E, March 2012



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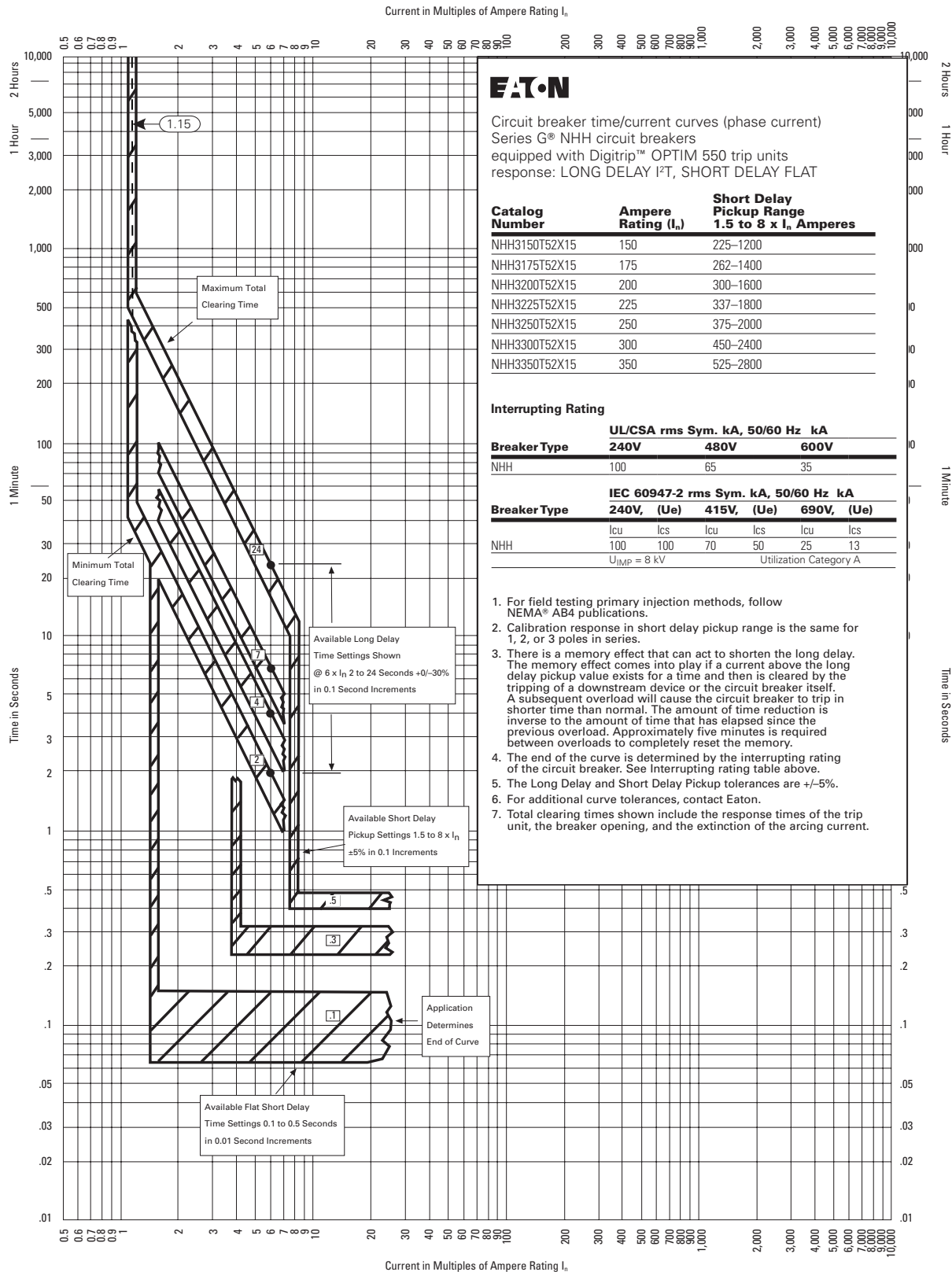
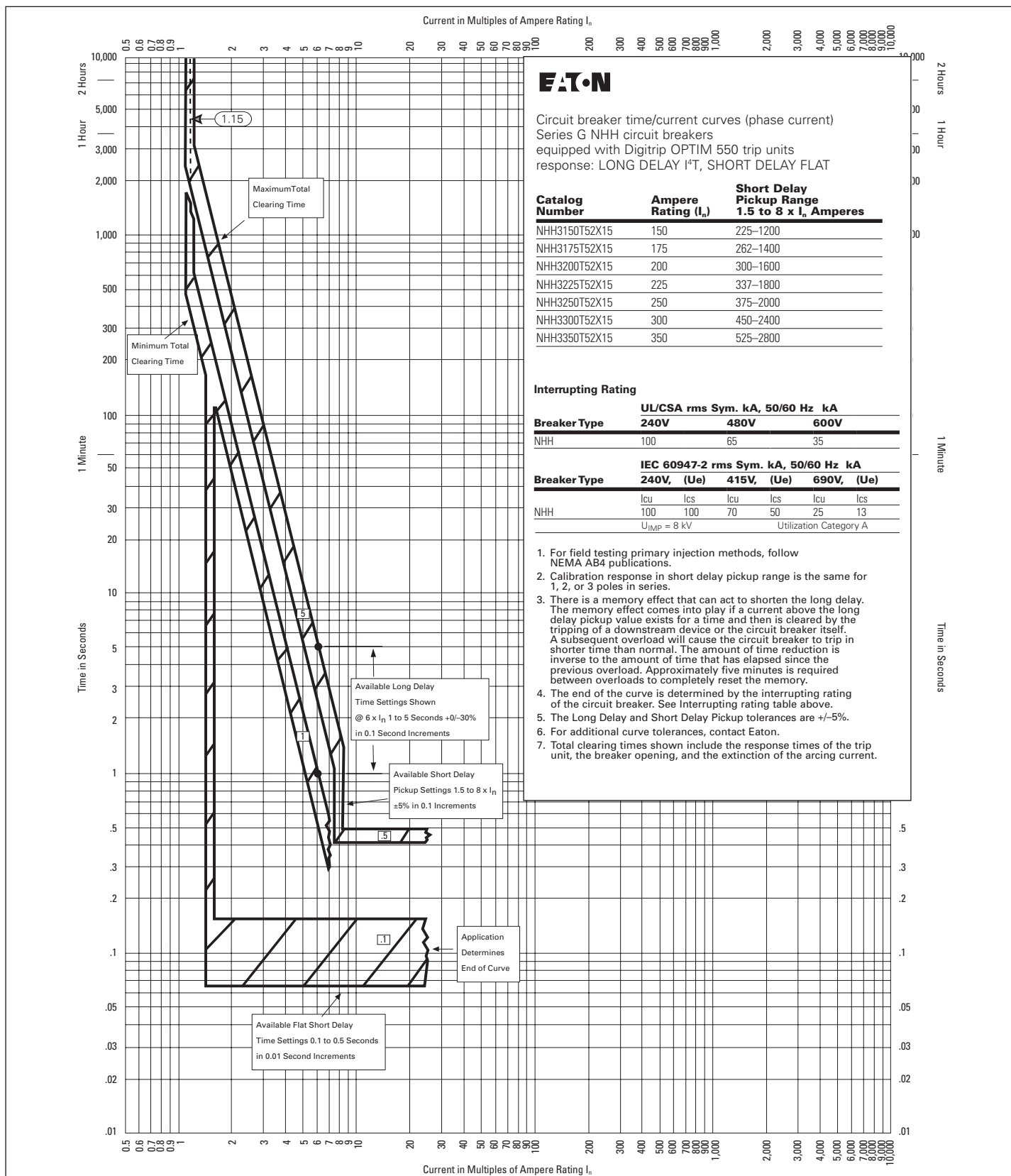


Figure 6. Digitrip OPTIM 550 NHH Long Delay I²T, Short Delay Flat NHH—Curve Number TC01207016E, September 2009

Figure 7. Digitrip OPTIM 550 NHH Long Delay I^2t , Short Delay Flat NHH—Curve Number TC01207017E, September 2009

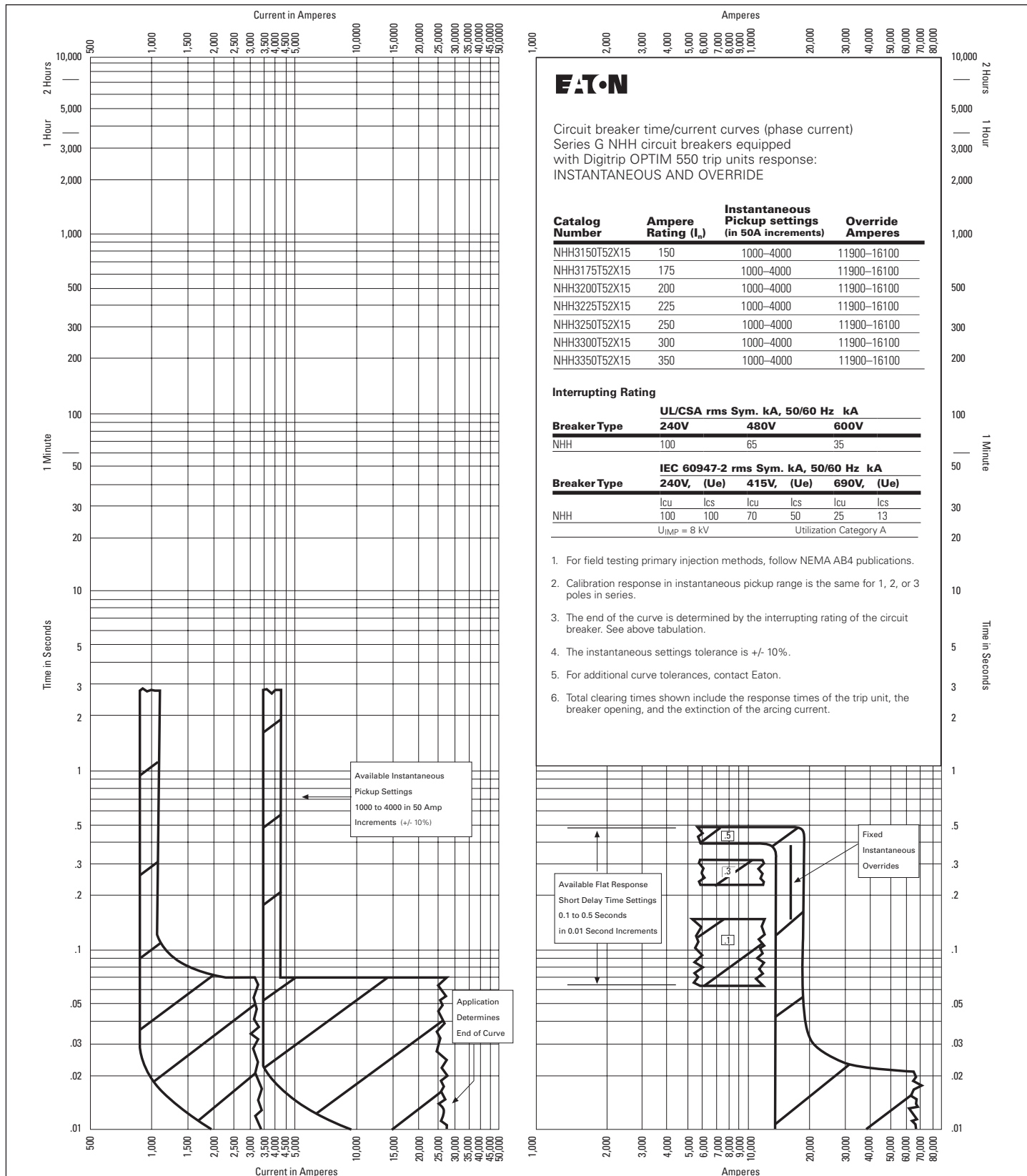
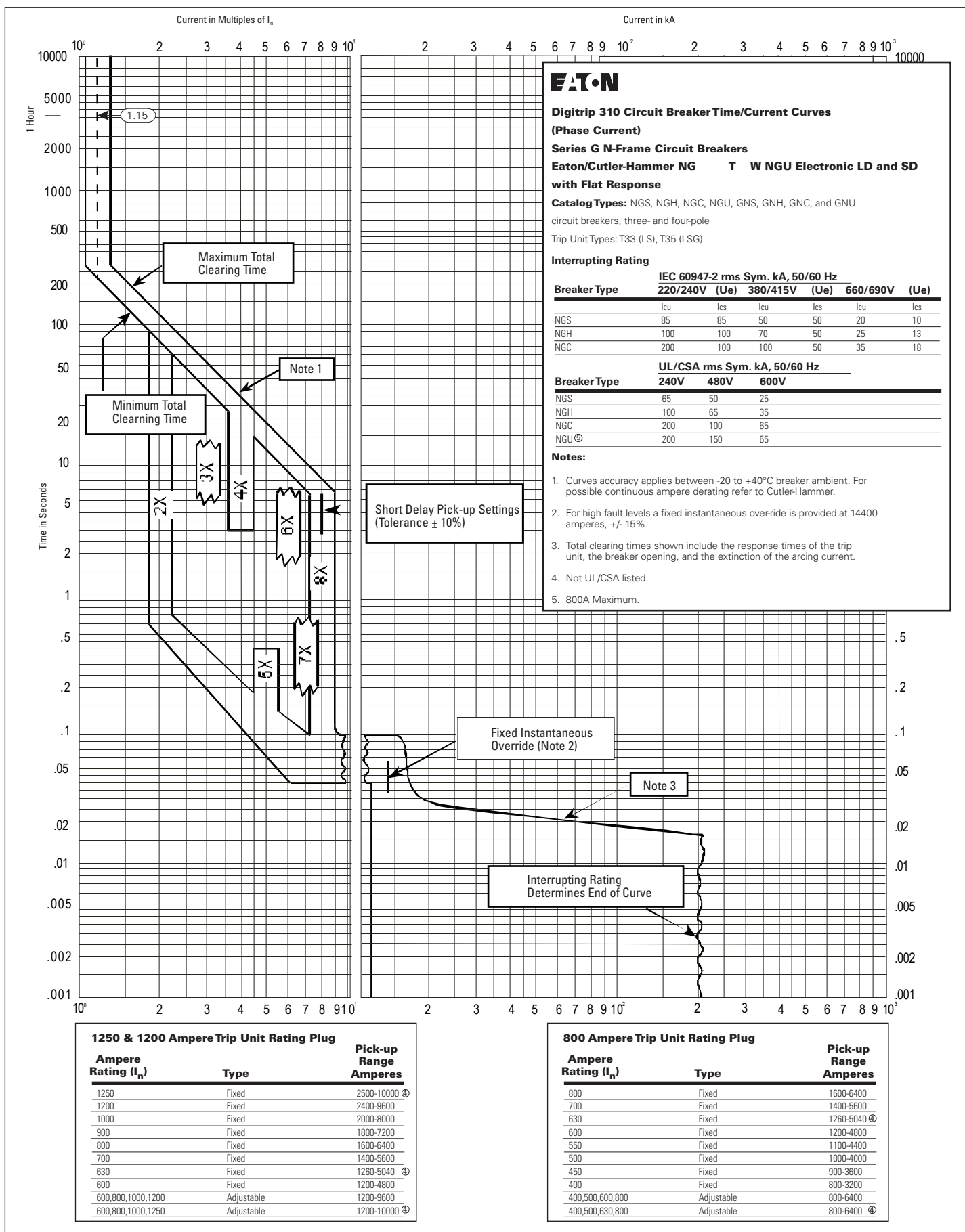


Figure 8. Digitrip OPTIM 550 NHH Instantaneous and Override NHH—Curve Number TC01207018E, September 2009

Figure 9. Digitrip 310 Long Delay and Short Delay with I²T Response (LS, LSG) - Curve Number TC01209003E

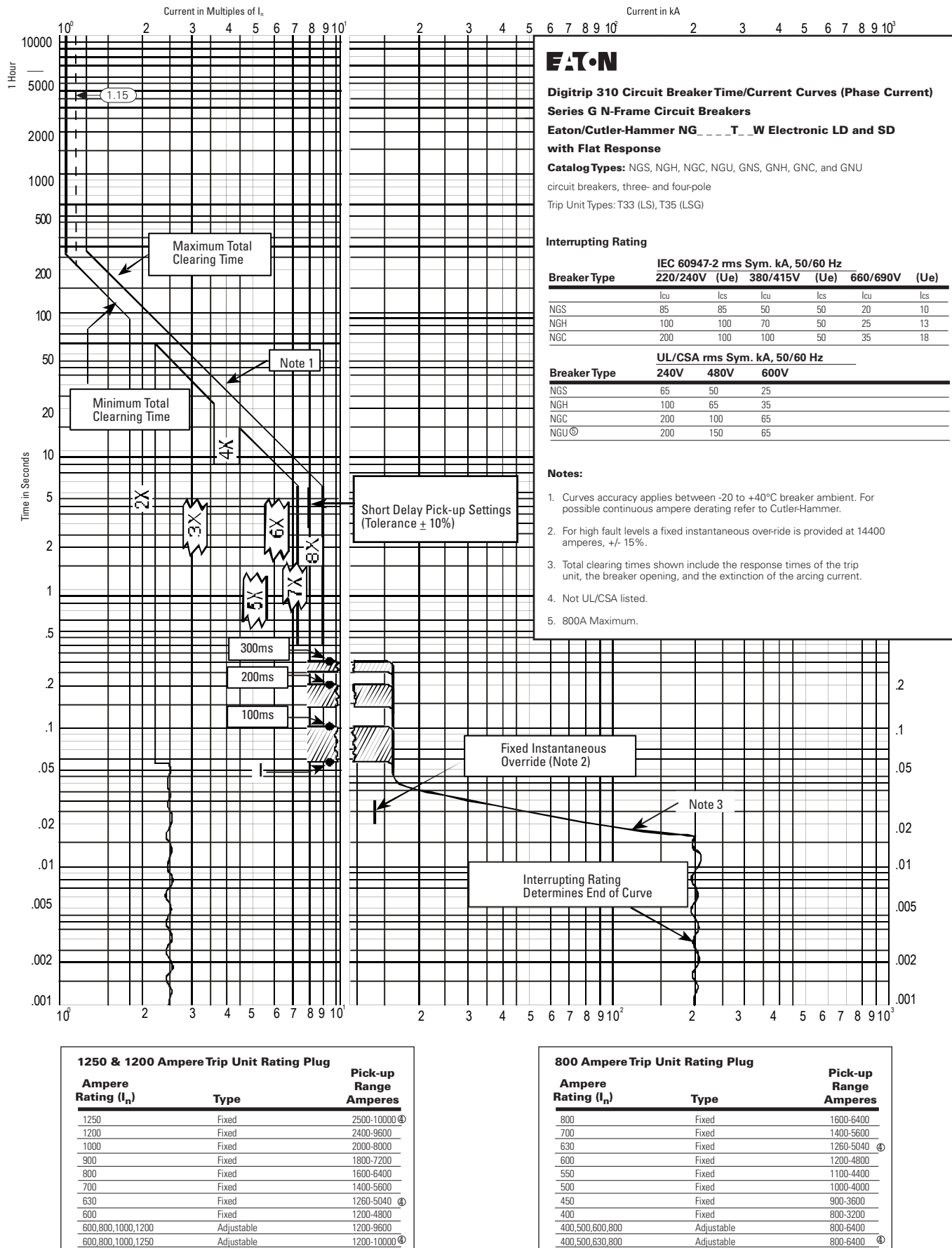


Figure 10. Digitrip 310 Long Delay and Short Delay with Flat Response (LSI, LSIG) - Curve Number TC01209004E

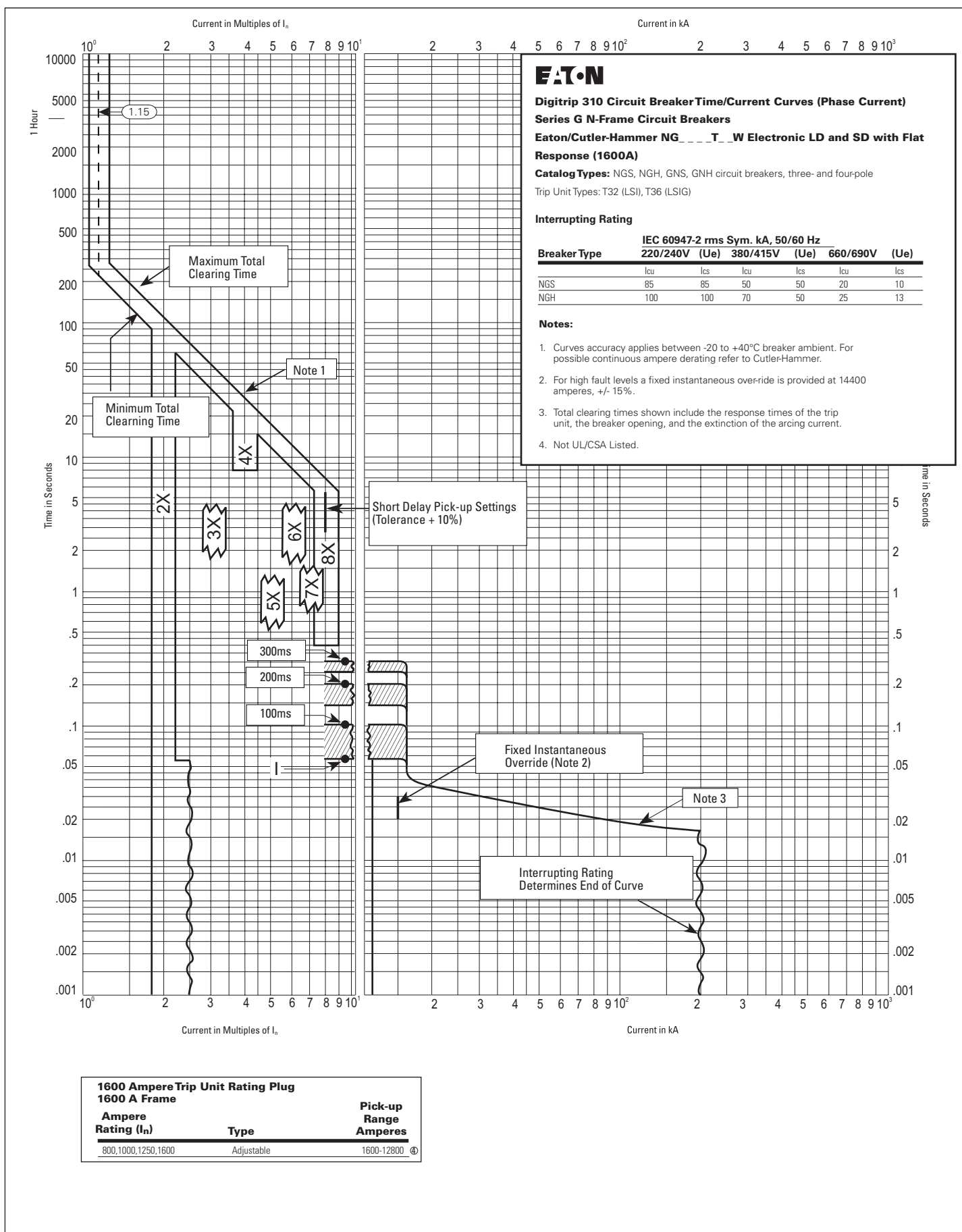
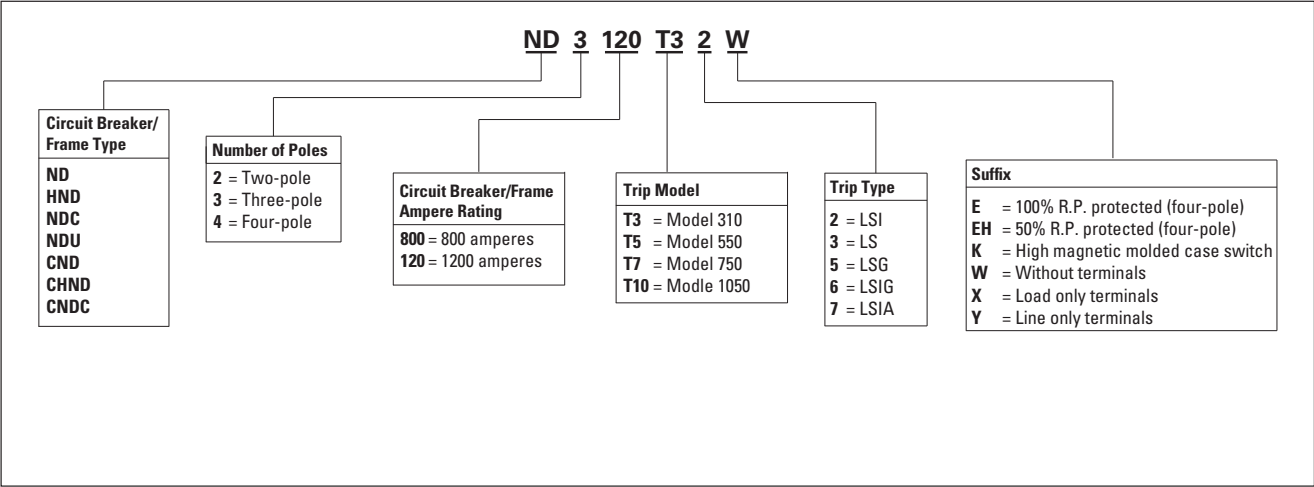


Figure 11. Digitrip 310 Long Delay and Short Delay with Flat Response (1600A) (LSI, LSIG) - Curve Number TC01209006E

Catalog Number Selection

This information is presented only as an aid to understanding catalog numbers.
It is not to be used to build catalog numbers for circuit breakers or trip units.



Types ND, CND, HND, CHND, NDC, CNDC, NDU, NGU Equipped With Type NES Digitrip RMS 310 Trip Units With I^2t Ramp Short Time Delay (Phase Protection)

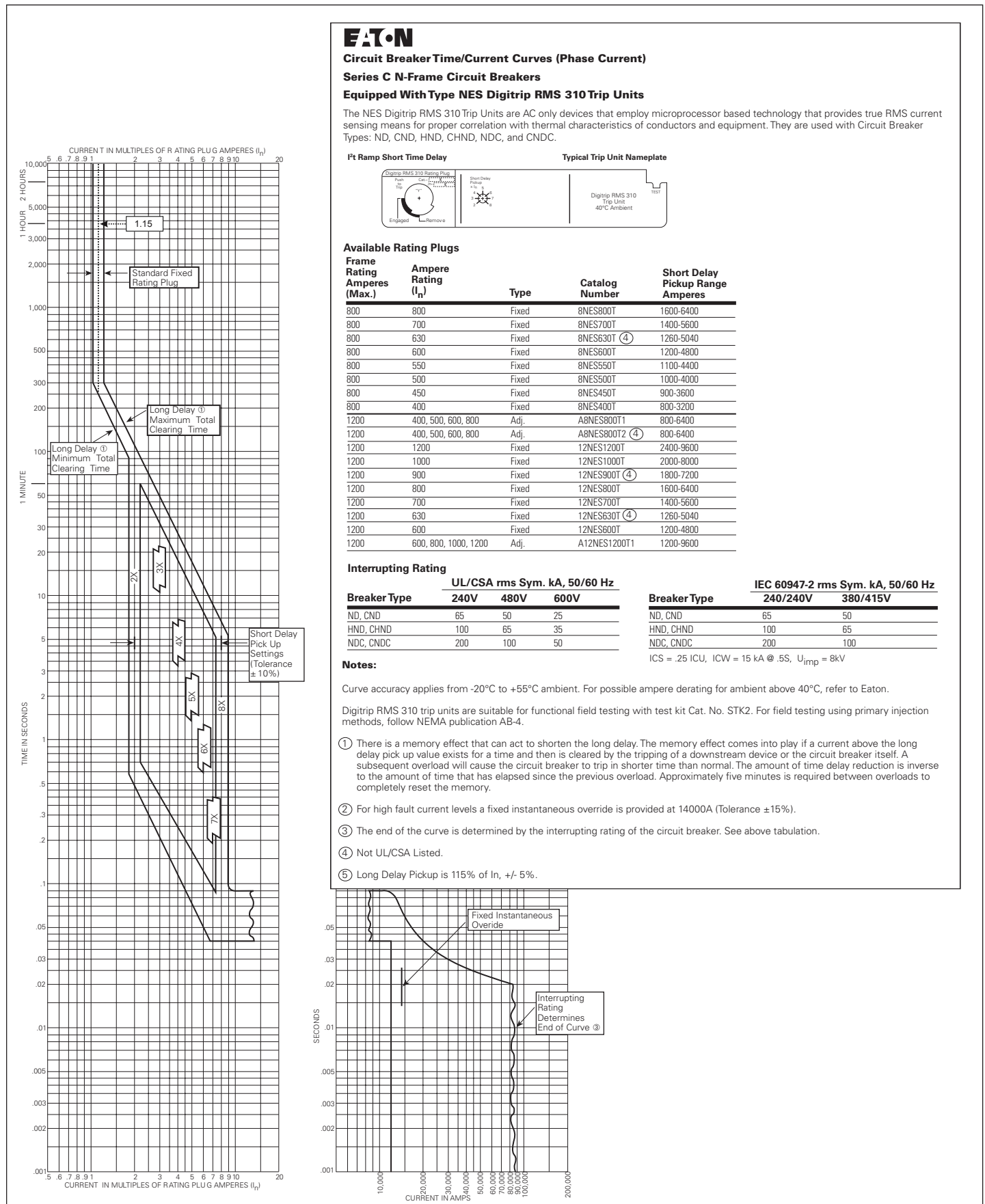


Figure 12. I^2t Ramp Short Time Delay (Phase Protection) - Curve Number SC-5375-92A, October 2006

Types ND, CND, HND, CHND, NDC, CNDC, NDU, NGU Equipped With Type NES Digitrip RMS 310 Trip Units With Adjustable Short Time Delay (Phase Protection)

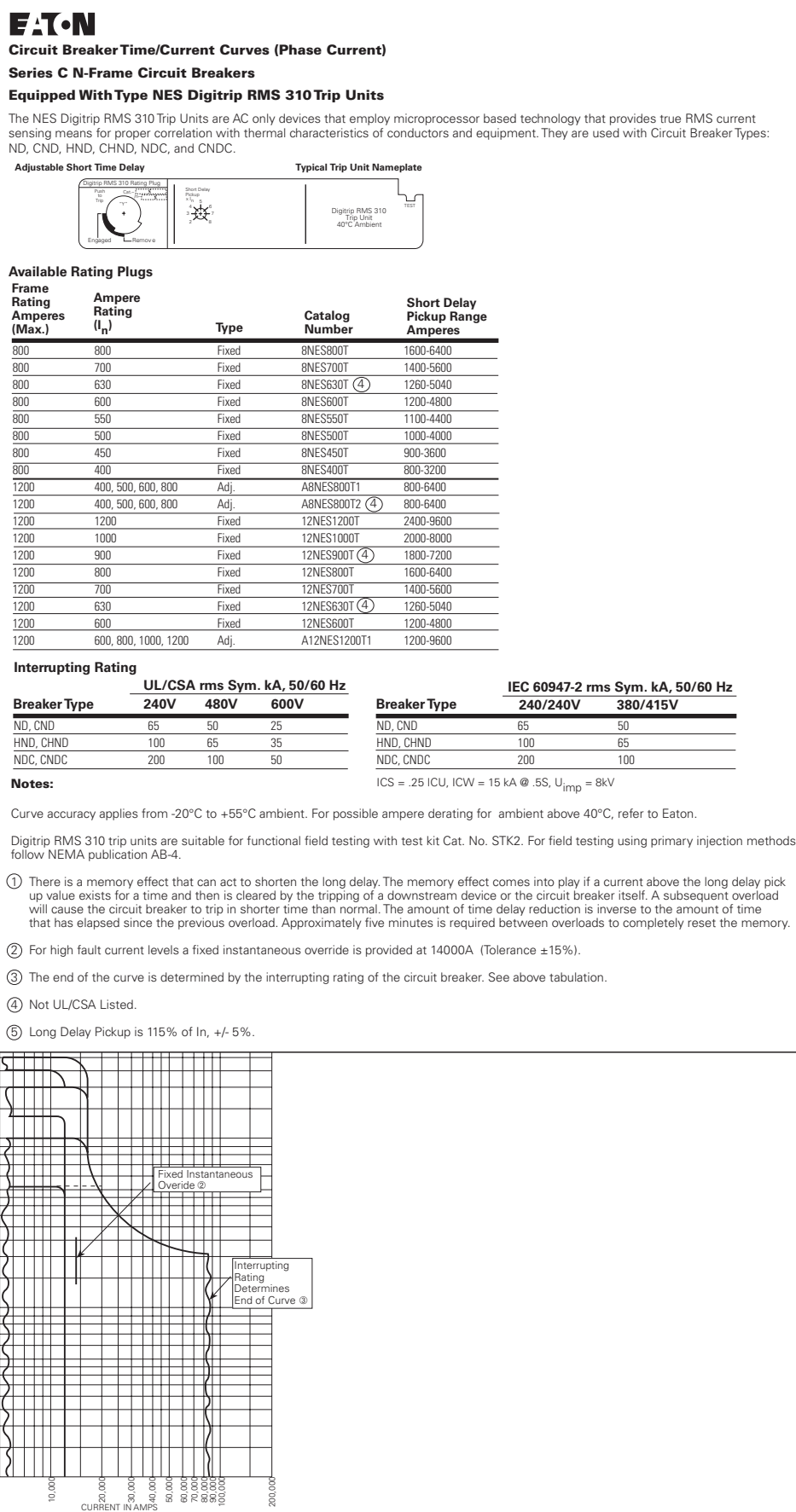


Figure 13. Adjustable Short Time Delay (Phase Protection) - Curve Number SC-5376-92A, October 2006

Types ND, CND, HND, CHND, NDC, CNDC, NDU, Equipped With Type NES Digitrip RMS 310 Trip Units With Ground Fault Protection

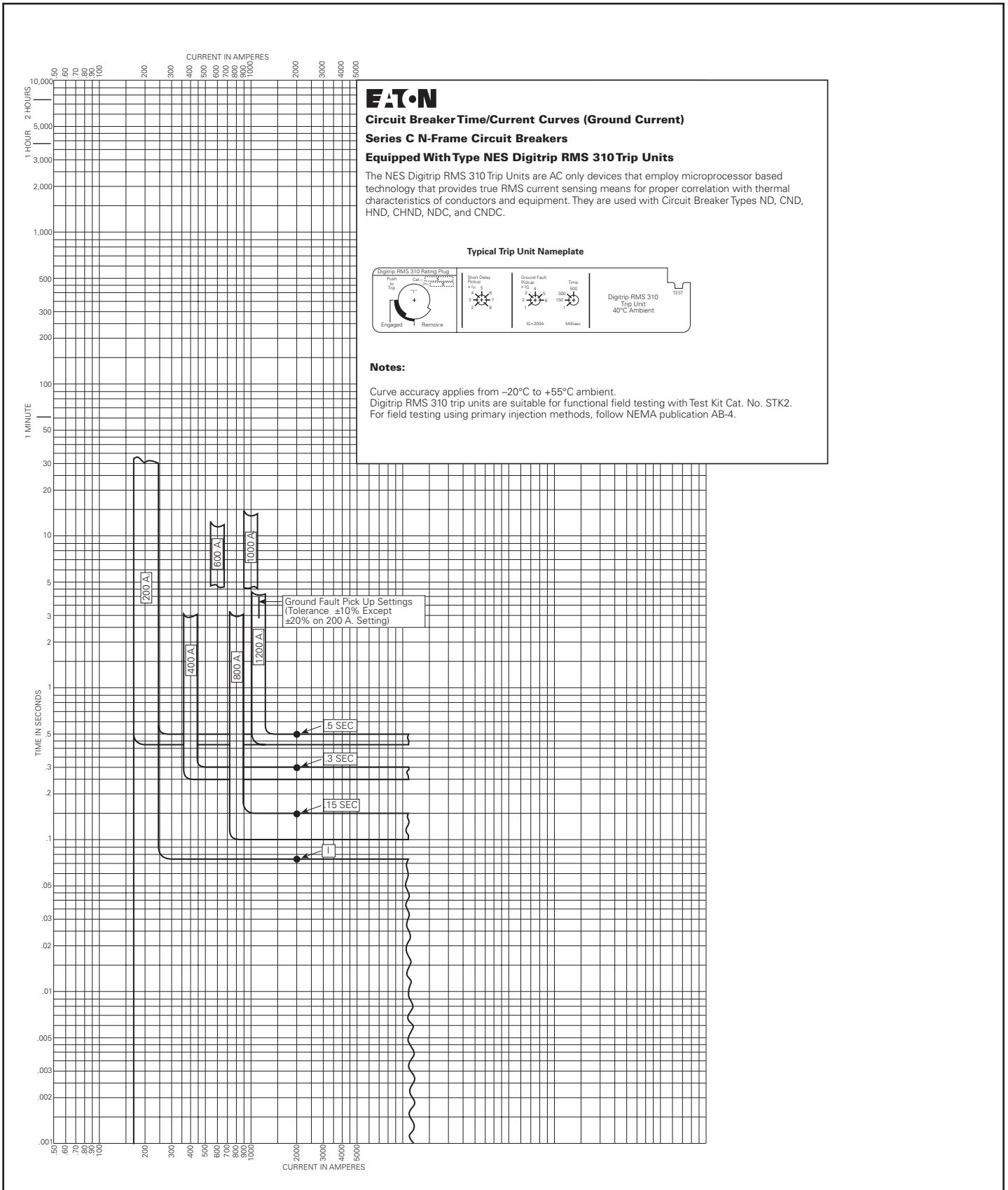


Figure 14. Ground Fault Protection - Curve Number SC-5377-92A, October 2009

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Long Delay I²t, Short Delay I²t

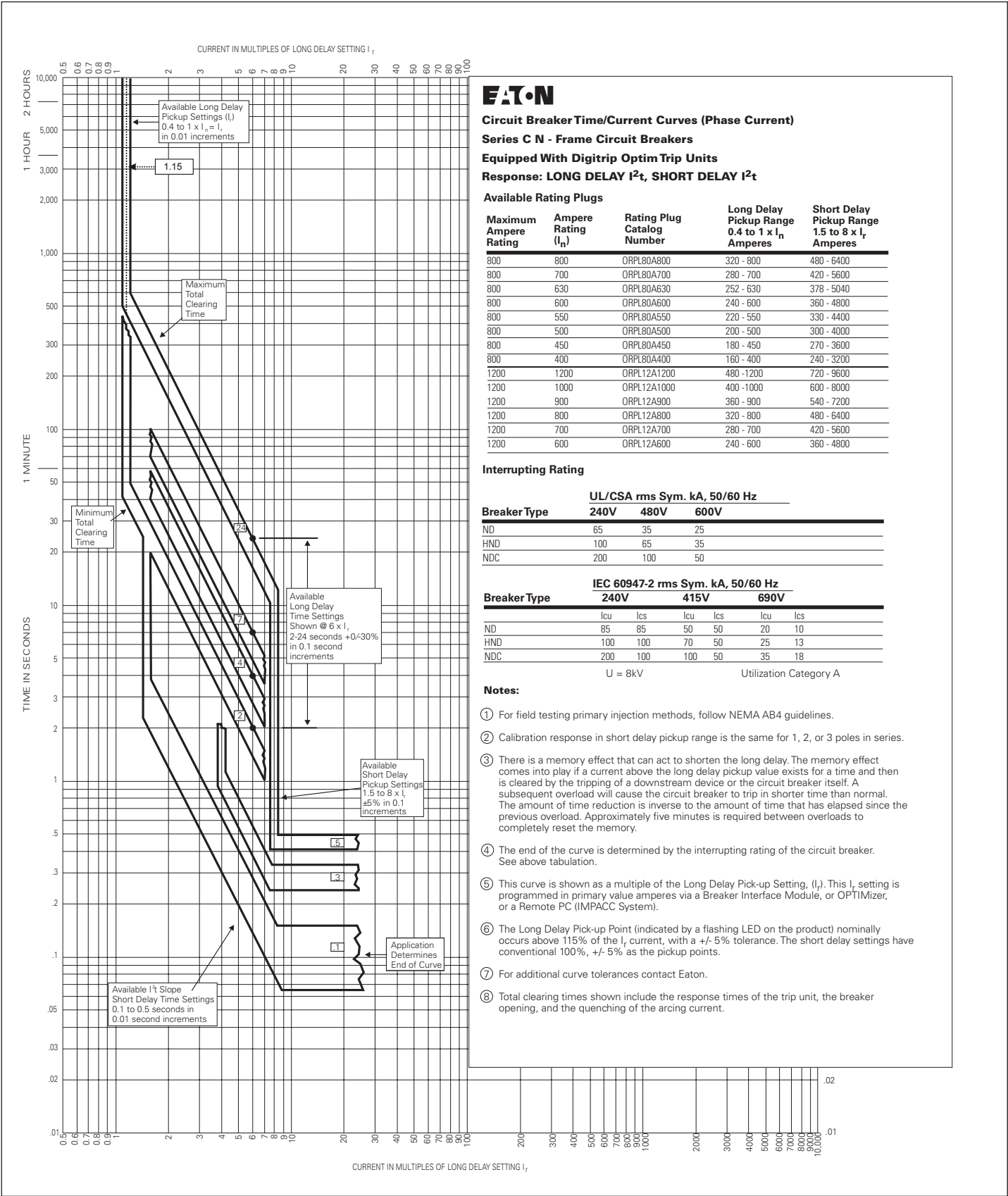
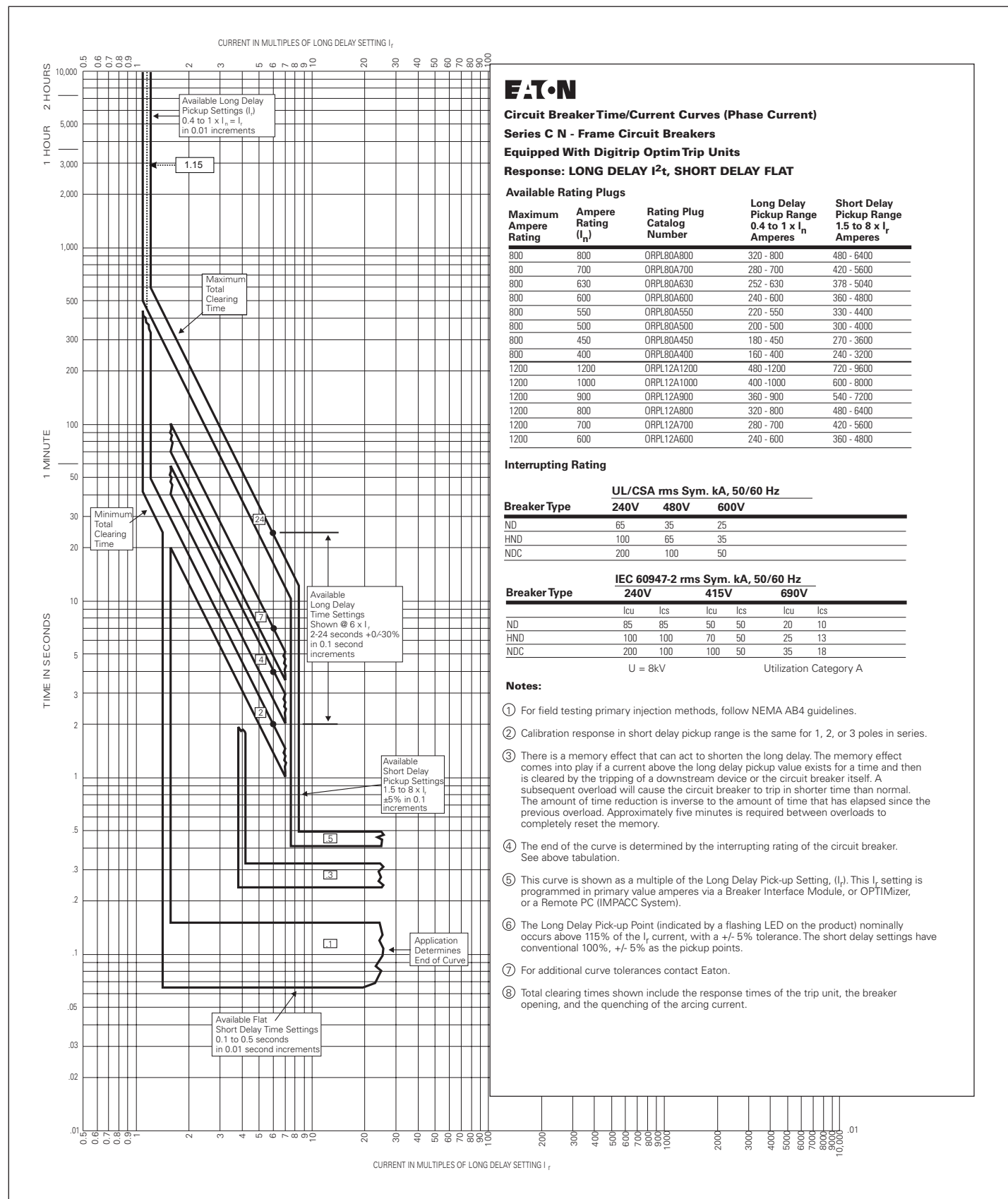


Figure 15. Long Delay I²T, Short Delay I²T - Curver Number SC-6331-96, October 2006

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Long Delay I²t, Short Delay FlatFigure 16. Long Delay I²t, Short Delay Flat - Curve Number SC-6332-96, October 2006

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Long Delay I⁴t, Short Delay Flat

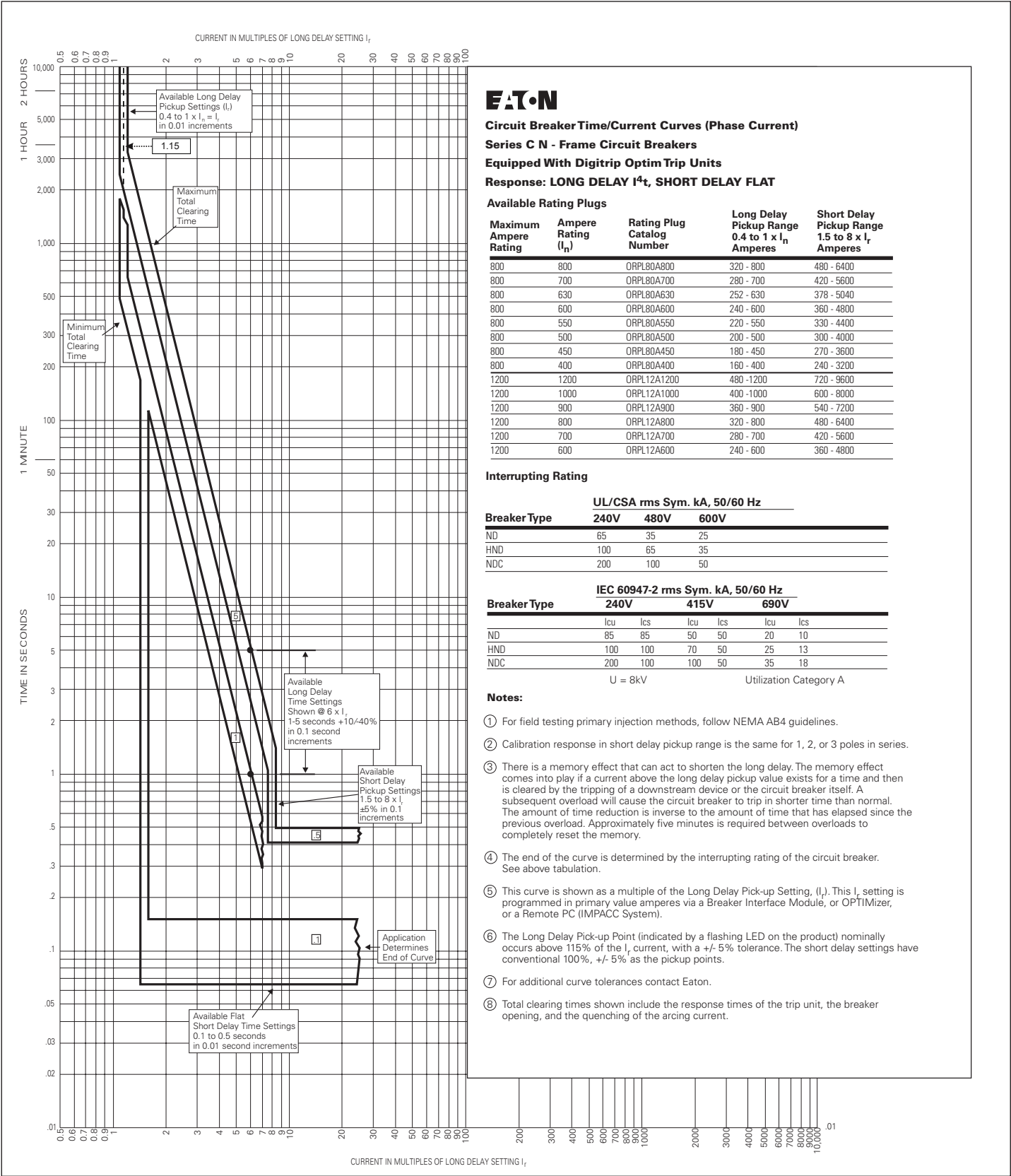


Figure 17. Long Delay I⁴t, Short Delay Flat - Curve Number SC-6333-96, October 2006

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Instantaneous and Override

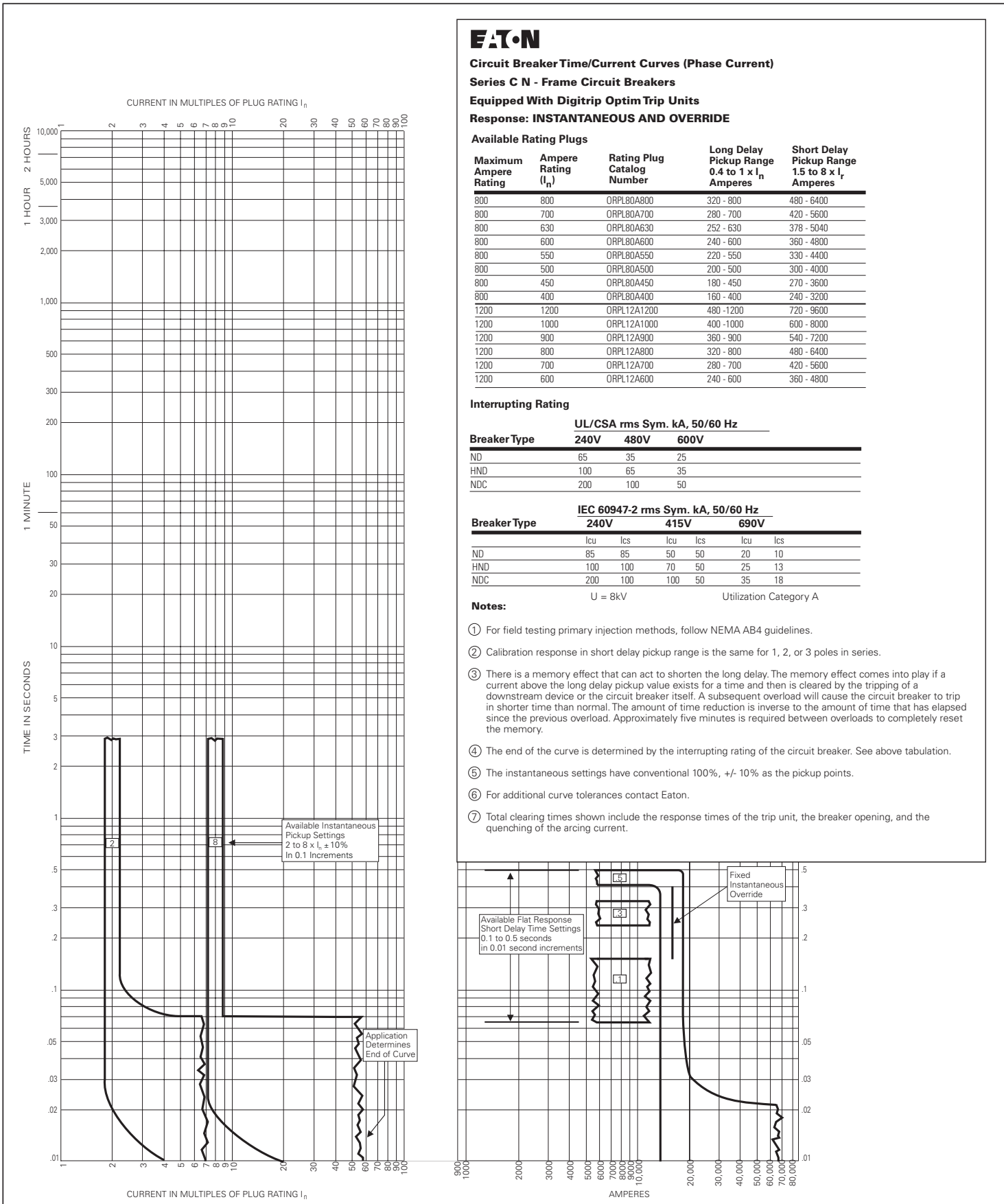
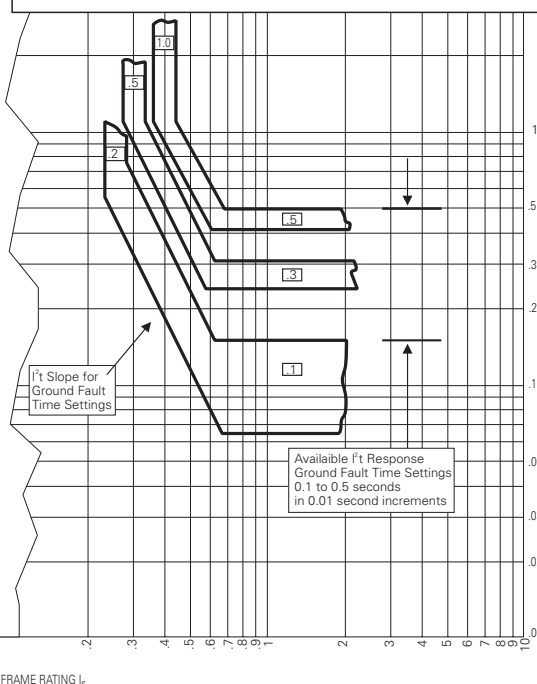


Figure 18. Instantaneous and Override - Curve Number SC-6334-96, October 2006

- ① For field testing primary injection methods, follow NEMA AB4 guidelines.
- ② Calibration response in short delay pickup range is the same for 1, 2, or 3 poles in series.
- ③ There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.
- ④ The end of the curve is determined by the interrupting rating of the circuit breaker.
See above tabulation.
- ⑤ The ground fault settings have conventional 100%, +/- 10% as the pickup points.
- ⑥ For additional curve tolerances contact Eaton.
- ⑦ Total clearing times shown include the response times of the trip unit, the breaker opening, and the quenching of the arcing current.

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